

Department of Electronics and Telecommunication Engineering

COLLEGE OF ENGINEERING PUNE

(An Autonomous Institute of Government of Maharashtra)

Time Table

Class : S. Y. B. Tech E & TC
With Effect From : 02.09.2020

Academic Year : 2020 - 2021
Term : I (First)

Day TIME	9.10 am To 9.55 am	10.00 am To 10.45 am	10.50 am To 11.35 pm	11.40am To 12.25pm	12.30 pm To 1.15 pm	1.15 pm To 2.00 pm	2.00 pm To 2.45 pm	2.50 pm To 3.35 pm	3.40 pm To 4.25 pm	4.25 pm To 5.10 pm	5.15 pm To 6.00pm
MON	LA & UC – DSY	FP (Foundations of Physics) – DSY	ODE & MVC (ABD)	S&S Tut (E) (SPMe)	DSD (VVI)	Lunch Break	EDC (VSA)	NSAF (RKP)	EDC (A), DSD (D), CS (B), S&A (C)		PLEVAH (Professional Laws, Ethics, Values and Harmony)
TUE	LA & UC – DSY	FP – DSY	ODE & MVC (ABD)	S&S (SPMe)	DSD (VVI)		EDC (VSA)	S&S Tut (B) (SPMe)	EDC (E), DSD (C), CS (A), S&A (D)		
WED	LA & UC – DSY	FP – DSY	ODE & MVC- TUT (ABD)	S&S (SPMe)	DSD (VVI)		EDC (VSA)	NSAF (RKP)	EDC (B), DSD (E), CS (C), S&A (A)		
THU	LA & UC – DSY	FP – DSY (Review Class)	ODE & MVC (ABD) (Review Class)	S&S (SPMe) (Review Class)	S&S Tut (A) (SPMe)		EDC (VSA) (Review Class)	NSAF (RKP)	EDC (C), DSD (B), CS (D), S&A (E)		
FRI	LA & UC – DSY (Review Class)		S&S Tut (D) (SPMe)	S&S Tut (C) (SPMe)	S&A-IFC (SSK)		DSD (VVI) (Review Class)	NSAF (RKP) (Review class)	EDC (D), DSD (A), CS (E), S&A (B)		
SAT				I&C (Innovation and Creativity) (11.45 to 12.30 pm)							

Electronic Devices & Circuits (EDC) (Th + Lab): Dr. Mrs. V. S. Agarwal, **Signals & Systems (S&S) (Th + Tut):** Dr. Ms. S. P. Metkar.

Digital System Design (DSD) (Th + Lab): Dr. Mrs. V. V. Ingale, **Network Synthesis and Analog Filter (NSAF) (Th):** Mrs. R. K. Patole,

Circuit Simulation (Lab): Ms. D. V. Niture, **Calculus:** Mrs. A. B. Dhere, **Sensors and Automation (S&A):** Mrs. S. S. Korde

In-charge, Timetable Committee,

Department of Electronics & Telecommunication Engineering

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COLLEGE OF ENGINEERING PUNE

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Class : T. Y. B. Tech E & TC
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Day TIME	9.10 am To 9.55 am	10.00 am To 10.45 am	10.50 am To 11.35 pm	11.40am To 12.25pm	12.30 pm To 1.15 pm	1.15 pm To 2.00 pm	2.00 pm To 2.45 pm	2.50 pm To 3.35 pm	3.35 pm To 4.20 pm	4.30 pm To 5.15 pm
MON			DSP (VSV)	RSSP (SGM)	CA (YMV)	Lunch Break	EMW (DVN)	CA(A), DSP(B)		*MINOR/ *HONOR
TUE			DSP (VSV)	RSSP (SGM)	DC (VNM)		EMW (DVN)	CA(B), DSP(C)		*MINOR/ *HONOR
WED			DSP (VSV)	CA (YMV)	DC (VNM)		EMW (DVN)	CA(C), DSP(D)		*MINOR/ *HONOR
THU		CI	RSSP (SGM)	CA (YMV)	DC (VNM)		EMW (DVN) <i>(Review Class)</i>	CA(D), DSP(E)		*MINOR/ *HONOR
FRI		RSSP (SGM) <i>(Review Class)</i>	DSP (VSV) <i>(Review Class)</i>	CA (YMV) <i>(Review Class)</i>	DC (VNM) <i>(Review Class)</i>		CA(E), DSP(A) 2:00 pm to 3:30 pm		ILOE (Humanities) 3:40 pm to 5.15 pm	
SAT	*Humanities-(Japanese) 9:00 am to 10:30 am			*Minor: (All branches): Monday to Thursday: 4.30 pm to 5.15 pm *Minor:(Mathematics) : Monday and Tuesday 5.15 pm to 6.00 pm, Wednesday 5.15 pm to 6.45 pm				ILOE (Humanities) 3:40 pm to 4:25 pm		

Digital Signal Processing (DSP) (Th + Tut): Dr. Mrs. V. S. Vyas, Digital Communication Systems (DC) (Th): Dr. Mrs. V. N. More

Computer Architecture (CA) (Th+Lab - batch-A): Mrs. Y. M. Vaidya, Computer Architecture (CA) (Lab-Batches B to E): Mrs. A. A. Kulkarni

Random Signal Stochastic Processes (RSSP): Mr. S. G. Mali, Electromagnetic Waves (EMW): Ms. D. V. Niture, Honor (Machine Learning): Ms. Meenal N.

Constitution of India (CI): Prof. Deshpande, Digital Signal Processing (DSP) (Lab): Dr. A. D. Gupta, Minor (E&TC- Microcontrollers): Mrs. A. A. Kulkarni

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Department of Electronics & Telecommunication Engineering

Department of Electronics and Telecommunication Engineering

COLLEGE OF ENGINEERING PUNE

(An Autonomous Institute of Government of Maharashtra)

Class : **Final B. Tech E & TC**
With Effect From : 02.09.2020

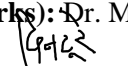
Academic Year : 2020 - 2021
Term : I (First)

Day TIME	10.00 am To 10.45 am	10.50 am To 11.35 pm	11.40am To 12.25pm	12.30 pm To 1.15 pm	1.15 pm To 2.00 pm	2.00 pm To 2.45 pm	2.50 pm To 3.35 pm	3.40 pm To 4.25 pm	4.30 pm To 5.15 pm	5.15 pm To 6.00pm
MON		CN(A), CMOS(E) 11.00 am To 12.30 pm			Lunch Break		Project	Project	*MINOR	
TUE	Project	MOC (SPM)	EL-II SP (RKP) CMOS (VSA)	AVE (ADG)		CN (RCM)	CN(B), SP(A) 2.50 pm To 4:20 pm		*MINOR	
WED	ILOE	MOC (SPM)	EL-II SP (RKP) CMOS (VSA)	AVE (ADG)		CN (RCM)	CN(C), SP(B) 2.50 pm To 4:20 pm		*MINOR	
THU	ILOE	MOC (SPM)	EL-II SP (RKP) CMOS (VSA)	AVE (ADG)		CN (RCM)	CN(D), SP(C) 2.50 pm To 4:20 pm		*MINOR	Project
FRI	ILOE	EL-II SP (RKP) CMOS (VSA) <i>(Review Class)</i>	CN(E), CMOS(D) 11.40 am To 1:10 pm			MOC (SPM) <i>(Review Class)</i>	CN (RCM) <i>(Review Class)</i>	AVE (ADG) <i>(Review Class)</i>	LLC	
SAT	ILOE	*Minor: (All branches): Monday to Thursday: 4:30 pm to 5:15 pm *Minor:(Mathematics) : Monday and Tuesday 5.15 pm to 6.00 pm, Wednesday 5.15 pm to 6.45 pm								

Microwave and Optical Communication (MOC) (Th): Dr. Mr. S. P. Mahajan, **Computer Networks (CN) (Th + Lab):** Dr. Mrs. R. C. Mahajan
Audio Video Engineering (AVE) (Th): Dr. A. D. Gupta

Elective-II: Speech Processing (SP) (Th): Mrs. R. K. Patole, **Speech Processing (SP) (Lab – Batches-A, B and C):** Prof. S. G. Mali,
Digital CMOS design (CMOS) (Th + Lab - – Batches-D and E): Dr. Mrs. V. S. Agarwal

***ILOE- Broadband Communication (Excluding E&TC students):** Dr. Mrs. P. P. Shingare, **Minor (E&TC – Wireless Sensor Networks):** Dr. Mrs. R. D. Joshi


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 Department of Electronics & Telecommunication Engineering