

Department of Electronics and Telecommunication Engineering

COLLEGE OF ENGINEERING PUNE

(An Autonomous Institute of Government of Maharashtra)

Time Table

Class : S. Y. B. Tech E & TC

With Effect From : 17.08.2023

Academic Year : 2023 - 2024

Term : I (First)

Day TIME	9.00 am To 10.00 am	10.00 am To 11.00 am	11.00 am To 12.00 pm	12.00 pm To 1.00 pm	1.00 pm To 2.00 pm	2.00 pm To 3.00 pm	3.00 pm To 4.00 pm	4.00 pm To 5.00 pm	5.00 pm To 6.00pm
MON	LA & UC – DSY	FP – DSY S&S Tut (A) (SPMe)	DSD (VSA) (ET-001)	EDC (SPM) (ET-001)	Lunch Break	NSAF (YDK) (ET-001)	EDC (A), DSD (B), CS (C), S&A (D), NSAF (E)		PLEVAH (ET-001) (SSD)
TUE	LA & UC – DSY	FP – DSY S&S Tut (B) (SPMe)	ODE & MVC (ABD) (ET-001)	EDC (SPM) (ET-001)		NSAF (YDK) (ET-001)	EDC (B), DSD (C), CS (D), S&A (E), NSAF (A)		
WED	LA & UC – DSY	FP – DSY S&S Tut (C) (SPMe)	ODE & MVC (ABD) (ET-001)	I&C ()		S&S (SPMe) (ET-001)	EDC (C), DSD (D), CS (E), S&A (A), NSAF (B)		
THU	LA & UC – DSY	S&S Tut (D) (SPMe)	S&S (SPMe) (ET-001)	EDC (SPM) (ET-001)		DSD (VSA) (ET-001)	EDC (D), DSD (E), CS (A), S&A (B), NSAF (C)		
FRI	LA & UC – DSY	S&A-IFC () (ET-001)	DSD (VSA) (ET-001)	ODE & MVC-TUT (ABD) S&S Tut (E) (SPMe)		NSAF (YDK) (ET-001)	EDC (E), DSD (A), CS (B), S&A (C), NSAF (D)		
SAT									

Electronic Devices & Circuits (EDC) (Th + Lab: Batches-A&B): Dr. S. P. Mahajan, **(EDC) (Lab: Batches-C, D & E):** Dr. Saiyma Fatima Raza

Digital System Design (DSD) (Th + Lab): Dr. Mrs. V. S. Agarwal, **Signals & Systems (S&S) (Th + Tut):** Dr. Ms. S. P. Metkar,

Network Synthesis and Analog Filter (NSAF) (Th +Lab): Mrs. Y. D. Kapse, **Circuit Simulation (Lab):** Mrs. A. G. Andurkar,

Sensors and Automation (S&A)(Th + Lab): (Instrumentation Dept.),

ODE & MVC (Th + Tut): Mrs. A. B. Dhere (Mathematics Dept.), **Innovation and Creativity (I&C):**

Professional Laws, Ethics, Values and Harmony (PLEVH)(TH):

For DSY: LA & UC (Th): (Mathematics Dept.), **Foundation of Physics (FP)(TH):** (Applied Science Dept.)